



01-22-04

Express Mail No. EV 346 810 379 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Bruno GHYSELEN et al.

Confirmation No.:

Application No.: 10/728,341

Group Art Unit:

Filing Date: December 3, 2003

Examiner:

For: METHOD FOR MANUFACTURING A
MULTILAYER SEMICONDUCTOR STRUCTURE
THAT INCLUDES AN IRREGULAR LAYER

Attorney Docket No.: 4717-12800

SUBMISSION OF CORRECTED DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith is one (1) sheet of formal drawings, including a correction to Fig. 5. The amended Fig. 5 shows the implantation zone 13 with a smoother configuration than in Fig. 4. Support for the corrected Fig. 5 can be found in the specification, page 9, line 29 to page 10, line 4. As no new matter is introduced, Applicants respectfully request that the attached drawings be substituted for the drawings originally filed in this application.

No fee is believed to be due for the present submission. If any fee should be required, however, please charge such fee to Winston & Strawn LLP Deposit Account No. 50-1814.

Respectfully submitted,

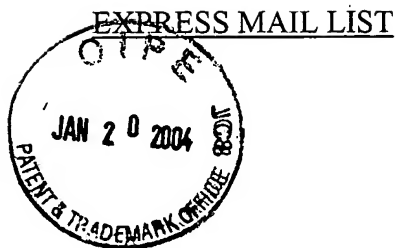
1/20/04
Date

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To:
Commissioner for Patents
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The following items listed below are being filed herewith with the USPTO on January 20, 2004

Express Mail No. EV 346 810 379 US		
Attorney Docket No.	Appln. Serial No./ Patent No.	Items - Documents filed on January 20, 2004
4717-12800	10/728,341	Submission of Corrected Drawings; Marked Copy of Fig. 5; Replacement Sheet for Fig. 5 (1 sheet)

*Please acknowledge receipt of these items as received by returning
the enclosed postcards with the date of receipt of January 20, 2004*

NY:833549.9